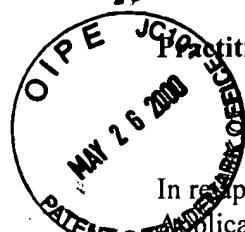


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Practitioner's Docket No. MI22-1322

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Agarwal, Vishnu K.  
Application No.: 09/512,149  
Filed: 02/23/2000  
For: Integrated Circuitry and Method of Forming a Capacitor

Group Art No.: 1554  
Examiner: Unknown

Assistant Commissioner for Patents  
Washington, D.C. 20231

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- There is an error with respect to the following, which is incorrectly entered.

*Error in*

1. Title

*Correct data*

2. Integrated Circuitry and Method of Forming a Capacitor

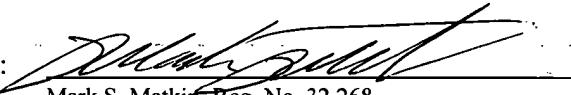
3. The correction is not due to any error by applicant and no fee is due.

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Telephone No.: (509) 624-4276

Customer No.: 021567

By:

  
Mark S. Matkin, Reg. No. 32,268  
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Bib Data Sheet

SERIAL NUMBER 09/512,149	FILING DATE 02/23/2000 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. MI22-1322
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**APPLICANTS**

Vishnu K Agarwal, Boise, ID ;

**\*\* CONTINUING DATA** *none, MDP***\*\* FOREIGN APPLICATIONS** *none, MDP***IF REQUIRED, FOREIGN FILING LICENSE****GRANTED \*\*** 04/24/2000

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verifier and Acknowledged <i>Vishnu K Agarwal</i> Examiner's Signature Initials		5	55	6

**ADDRESS**

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**TITLE**

Integrated circuitry and method of forming a capacitor

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